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Haratsch 1-4

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Erich Franz Haratsch,  
Harish Viswanathan  
Case: 1-4  
Serial No.: 09/326,785  
Filing Date: June 4, 1999  
Group: 2776  
Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

Signature: [Signature] Date: July 17, 2001

Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying PTO Form 1449. Copies of the listed items are enclosed.

U.S. Patents:

1. United States Patent No. 5,870,433 to Huber et al.
2. United States Patent No. 6,035,006 to Matui
3. United States Patent No. 6,201,831 to Agazzi et al.

Other Documents:

4. Lee et al., "A Maximum-Likelihood Sequence Estimator with Decision-Feedback Equalization," IEEE Transactions on Communications, Vol.Com-25, No. 9, 971-979 (September 1997)

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,

[Signature]

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Date: July 17, 2001